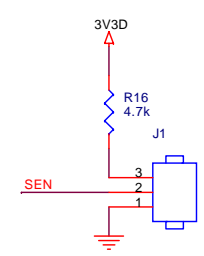
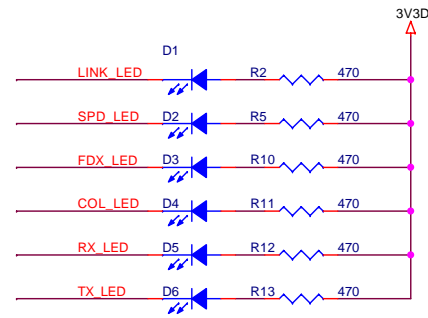
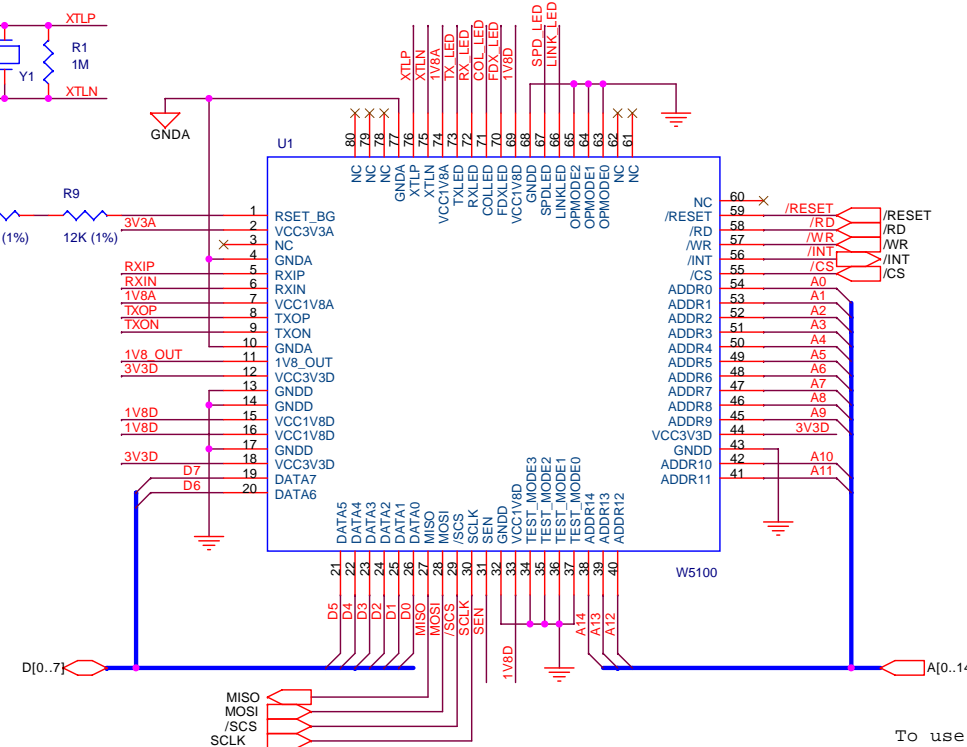
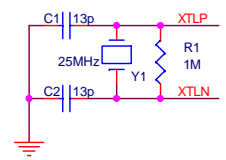
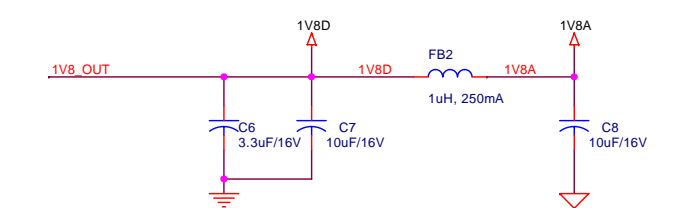


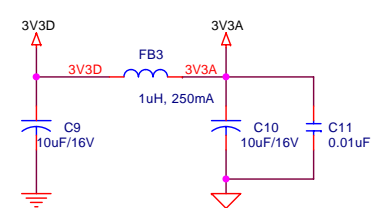
CONNECT TO CHASSIS GROUND



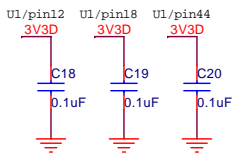
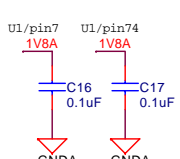
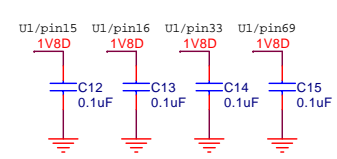
To use the SPI interface, pull up the SEN pin. Otherwise pull down the SEN. When using SPI mode, pull down the ADDR pins.



Place C6, C7, FB2 close to 1V8OUT and place C8 close to 1V8A



Place C9, FB3 close to 3V3D and place C10, C11 close to 3V3A



Place C12, C13, C14, C15, C16, C17, C18, C19, C20 as close to each power pin as possible.

|  |                                   |     |
|--|-----------------------------------|-----|
| Title                                    |                                   |     |
| W5100 Reference schematic (use MAG JACK) |                                   |     |
| Size                                     | Document Number                   | Rev |
| B  | <Doc>                             | 2.1 |
| Date:                                    | Tuesday, February 02, 2010 1 of 1 |     |

< DIFFERENCES BETWEEN VER.2.0 and VER.2.1 >

1. Separated common ground into 'analog' and 'digital' ground. It may use common ground as your design.
2. Modified net name of U1.31 from SPI\_EN to SEN.
3. Changed values of C1 and C2 in CRYSTAL circuit. It can be better performed 'auto-negotiation' function.
4. Reinforced power circuit.(refer to below table)

|              | Rev2.0 | Rev2.1 |
|--------------|--------|--------|
| 3.3V circuit |        |        |
| 1.8V circuit |        |        |

|                |                            |              |
|----------------|----------------------------|--------------|
| Title          |                            |              |
| update history |                            |              |
| Size           | Document Number            | Rev          |
| B              | <Doc>                      | <RevCode>    |
| Date:          | Tuesday, February 02, 2010 | Sheet 1 of 1 |