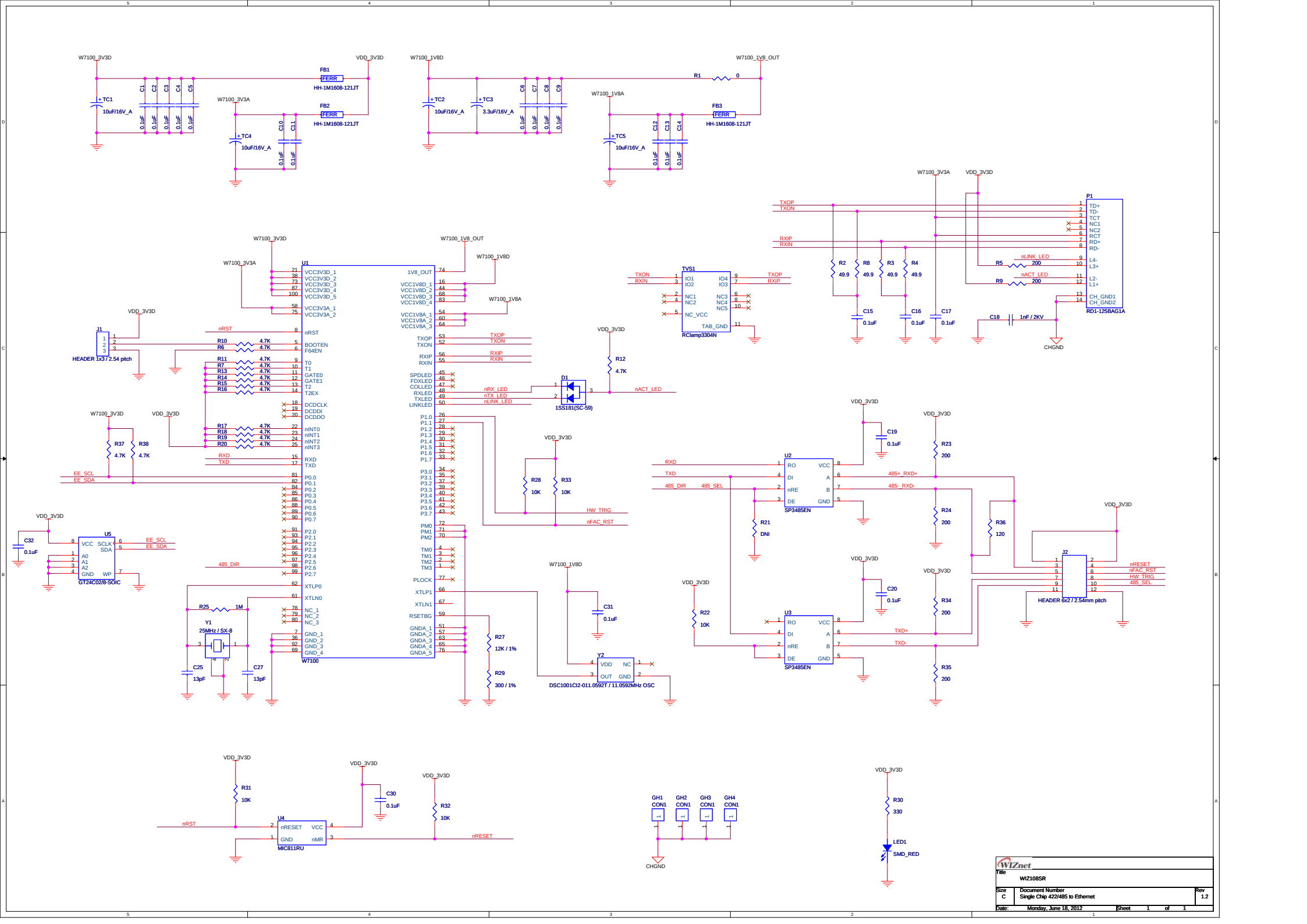
W7100_3V3D
VDD_3V3D
FB1
FERR
HH-1M1608-121JT
FB2
FERR
HH-1M1608-121JT
TC1
10uF/16V_A
C1
C2
C3
C4
C5
0.1uF
W7100_3V3A
TC4
10uF/16V_A
C10
C11
W7100_1V8D
TC2
10uF/16V_A
TC3
3.3uF/16V_A
C6
C7
C8
C9
W7100_1V8_OUT
R1
0
W7100_1V8A
TC5
10uF/16V_A
C12
C13
C14
FB3
FERR
HH-1M1608-121JT
P1
TD+
TD-
TCT
NC1
NC2
RCT
RD+
RD-
L4-
L3+
L2-
L1+
CH_GND1
CH_GND2
RD1-125BAG1A
TXOP
TXON
RXIP
RXIN
R2
49.9
R8
49.9
R3
49.9
R4
49.9
R5
200
R9
200
nLINK_LED
nACT_LED
C15
0.1uF
C16
0.1uF
C17
0.1uF
C18
1nF / 2KV
CHGND
TVS1
IO1
IO2
IO4
IO3
NC1
NC2
NC3
NC4
NC5
NC_VCC
TAB_GND
RClamp3304N
U1
VCC3V3D_1
VCC3V3D_2
VCC3V3D_3
VCC3V3D_4
VCC3V3D_5
VCC3V3A_1
VCC3V3A_2
1V8_OUT
VCC1V8D_1
VCC1V8D_2
VCC1V8D_3
VCC1V8D_4
VCC1V8A_1
VCC1V8A_2
VCC1V8A_3
nRST
BOOTEN
F64EN
T0
T1
GATE0
GATE1
T2
T2EX
DCDCLK
DCDDI
DCDDO
nINT0
nINT1
nINT2
nINT3
RXD
TXD
P0.0
P0.1
P0.2
P0.3
P0.4
P0.5
P0.6
P0.7
P2.0
P2.1
P2.2
P2.3
P2.4
P2.5
P2.6
P2.7
XTLP0
XTLN0
NC_1
NC_2
NC_3
GND_1
GND_2
GND_3
GND_4
W7100
SPDLED
FDXLED
COLLED
RXLED
TXLED
LINKLED
P1.0
P1.1
P1.2
P1.3
P1.4
P1.5
P1.6
P1.7
P3.0
P3.1
P3.2
P3.3
P3.4
P3.5
P3.6
P3.7
PM0
PM1
PM2
TM0
TM1
TM2
TM3
PLOCK
XTLP1
XTLN1
RSETBG
GNDA_1
GNDA_2
GNDA_3
GNDA_4
GNDA_5
J1
HEADER 1x3 / 2.54 pitch
R10
4.7K
R6
4.7K
R11
R7
R13
R14
R15
R16
R17
R18
R19
R20
R37
4.7K
R38
4.7K
EE_SCL
EE_SDA
D1
1SS181(SC-59)
nRX_LED
nTX_LED
nLINK_LED
R12
4.7K
nACT_LED
R28
10K
R33
10K
HW_TRIG
nFAC_RST
485_DIR
R25
1M
Y1
25MHz / SX-8
C25
13pF
C27
13pF
R27
12K / 1%
R29
300 / 1%
C31
0.1uF
Y2
VDD
NC
OUT
GND
DSC1001CI2-011.0592T / 11.0592MHz OSC
U5
VCC
SCLK
SDA
A0
A1
A2
GND
WP
GT24C02/8-SOIC
C32
0.1uF
U2
RO
DI
nRE
DE
VCC
A
B
GND
SP3485EN
RXD
TXD
485_DIR
485_SEL
485+_RXD+
485-_RXD-
R21
DNI
C19
0.1uF
R23
200
R24
200
R36
120
U3
SP3485EN
TXD+
TXD-
R22
10K
C20
0.1uF
R34
200
R35
200
J2
HEADER 6x2 / 2.54mm pitch
nRESET
nFAC_RST
HW_TRIG
485_SEL
U4
nRESET
VCC
GND
nMR
MIC811RU
R31
10K
C30
0.1uF
R32
10K
GH1
CON1
GH2
CON1
GH3
CON1
GH4
CON1
R30
330
LED1
SMD_RED
WIZnet
Title
WIZ108SR
Size
C
Document Number
Single Chip 422/485 to Ethernet
Rev
1.2
Date:
Monday, June 18, 2012
Sheet
1
of
1