

NOTES: UNLESS OTHERWISE SPECIFIED;

- 1. ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 5%
- 2. ALL CAPACITORS ARE 16VDC OR HIGHER.
- 3. THE ORIGINATION SOURCE OF A VOLTAGE IS REPRESENTED BY (vcc), AND ALL REFERENCES TO THAT VOLTAGE

 ARE REPRESENTED BY (vcc).

OUTLINED CIRCUIT MAY NOT BE STUFFED DEPENDING ON MODEL, SEE STUFFING CHART FOR CLARIFICATION.

5. COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED DEPENDING ON MODEL.

APPEND THE FOLLOWING	DRAWING CONTENT:		TITLE			
DOCUMENTS WHEN CHANGI	TG DRAWN BY: (INITIAL RELEASE)		1			
THIS DOCUMENT:	J. FENG	1/30/04	SCH	FΜ	ATIC DIAGRAM	RABBIT
	REVISED BY:		BL2600		NADDII SEMICONDUCTOR	
	J. NAUER	4/23/09			3L2600	SEMICONDUCTOR
	APPROVALS: INITIAL RELEASE					
	PROJECT ENGINEER:					www.rabbit.com
	J. FENG	3/15/04	SIZE	DWG NO.	000	٥ ـ
	ENGINEERING MANAGER:		1 ('		090-01	95
	XUAN TRUONG	7/22/08				5
	SIGNATURES	DATE	SCALE NON	Е	RELEASE DATE	SHEET 1 OF 5









